

Amendments to the Claims:

Please amend the claims as follows:

1. (Currently Amended) A receiver comprising video signal error detection system for use in a receiver having a decoder for receiving encoded video signals which include produced by an encoder in a transmitter, the encoder being selectively operative to transmit only differences between selected macroblocks in successive frames when a specific difference criteria between said successive blocks is not exceeded, the receiver also comprising a video signal error detection system comprising a first comparator for comparing macroblocks in successive frames in the output signal from said decoder, and applying said specific difference criteria to provide an indication of whether inter-frame prediction should apply or not, and a second comparator for comparing the indication output from the first comparator with an output direct from the decoder indicative of whether or not the last received macroblock received was encoded in inter-frame prediction format or not, the second comparator being operative and operable to generate an error signal when a divergence is detected.

2. (Currently Amended) A system according to Claim 1, including an error concealment circuit responsive to the output of the decoder and a previous frames buffer to recover corrupted data when the second comparator indicates an error.

3. (Previously Amended) A system according to Claim 1, wherein the first comparator acts as a mode decision circuit capable of generating one of three outcomes namely, inter-frame prediction, no inter-frame prediction and unknown.

4. (Currently Amended) A system according to Claim 3, wherein the mode decision circuit responds to each macroblock of each frame from the output from the decoder to determine a mean value for ~~the~~ pixels and responds to at the difference A between ~~the~~ reconstructed values of ~~the~~ pixels in said macroblock and said mean value, at the difference B between the reconstructed values of ~~the~~ pixels of the present macroblock and ~~the~~ reconstructed values of ~~the~~ pixels of the corresponding macroblock

of ~~an~~the immediately preceding frame and ~~an~~the error margin E to provide a first outcome if $A < B - E$ a second outcome if $B - E < A < B + E$ and a third outcome if $B + E < A$.

5. (Original) A system according to Claim 2, wherein the error concealment circuit acts to replace each corrupted macroblock with a corresponding macroblock in the immediately preceding frame.

6. (Newly Added) A video signal error detection system for use in a receiver having a decoder for receiving encoded video signals produced by an encoder in a transmitter, the encoder being selectively operative to transmit only differences between selected macroblocks in successive frames when a specific difference criteria between said successive blocks is not exceeded, the detection system comprising a first comparator for comparing macroblocks in successive frames in an output signal from said decoder, applying said specific difference criteria to provide an indication of whether inter-frame prediction should apply or not, and a second comparator for comparing the indication from the first comparator with an output from the decoder indicative of whether or not the last received macroblock was in inter-frame prediction format or not and operable to generate an error signal when a divergence is detected; wherein the first comparator acts as a mode decision circuit capable of generating one of three outcomes namely, inter-frame prediction, no inter-frame prediction and unknown; wherein the mode decision circuit responds to each macroblock of each frame from the output from the decoder to determine a mean value for pixels and responds to a difference A between reconstructed values of pixels in said macroblock and said mean value, a difference B between the reconstructed values of pixels of the present macroblock and reconstructed values of pixels of the corresponding macroblock of an immediately preceding frame and an error margin E to provide a first outcome if $A < B - E$ a second outcome if $B - E < A < B + E$ and a third outcome if $B + E < A$.

7. (Newly Added) A mobile receiver comprising:
a decoder which acts to reconstitute received compressed video signals using

signals stored in a buffer representing a previous picture frame, the video signals having been compressed using inter-frame prediction;

the receiver further comprising a mode decision circuit configured to compare a signal at the output of the decoder with a previous signal stored in the buffer and using the same decision criterion as used for the compression to generate a further signal which indicates that either the signal at the output of the decoder is suitable for inter-frame prediction or not; and

the receiver further comprising a comparator configured to compare said further signal with a signal from the decoder indicating whether or not inter-frame prediction was used for the compression, wherein if equality is detected then the receiver generates an error signal.

8. (Newly Added) A receiver according to claim 7 further comprising an error concealment circuit responsive to the error signal to recover corrupted data from the signal at the output of the detector and the previous signal stored in the buffer.

9. (Newly Added) A receiver according to claim 8 wherein the error concealment circuit acts to replace each corrupted macroblock with a corresponding macroblock in the immediately preceding frame.